

## **Advanced Facility Design Drivers**

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Please copy-and-paste your abstract into this space. Paper will address the anticipated design requirements for Semiconductor factories at advanced technology nodes of 65 nano-meter and beyond. The paper will discuss projections for building volumetric requirements. In addition, technical issues related to the requirements of the cleanroom will be developed. Particular attention will be given to requirements and practices in Asia (excluding Japan). These discussions will be based on experience gained from 300 mm factory design. Order of magnitude projections of the requirements for 450 mm fabs will be opened for discussion.